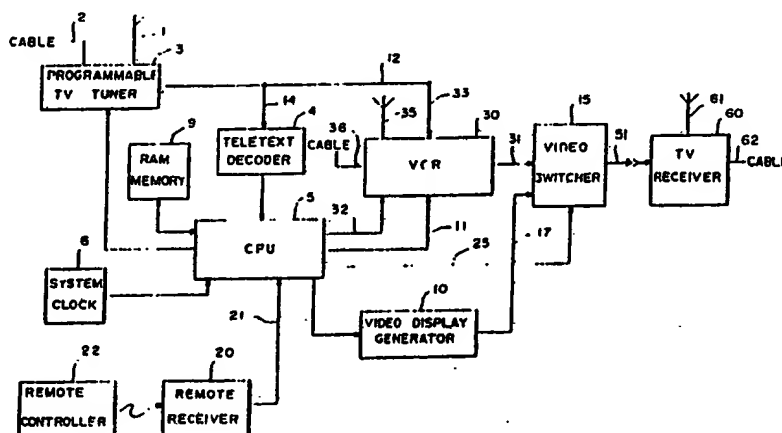




INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(51) International Patent Classification 4 : H04N 7/087, 7/04, G11B 27/02 H04H 1/00	A1	(11) International Publication Number: WO 90/00847 (43) International Publication Date: 25 January 1990 (25.01.90)
(21) International Application Number: PCT/US89/02927 (22) International Filing Date: 10 July 1989 (10.07.89) (30) Priority data: 219,971 15 July 1988 (15.07.88) US (71) Applicant: INSIGHT TELECAST, INC. [US/US]; 1496 Cherrywood Drive, San Mateo, CA 94403 (US). (72) Inventor: YOUNG, Patrick ; 1496 Cherrywood Drive, San Mateo, CA 94403 (US). (74) Agents: NISHIMURA, Keiichi et al.; Flehr, Hohbach, Test, Albritton & Herbert, Four Embarcadero Center, Suite 3400, San Francisco, CA 94111-4187 (US).		(81) Designated States: AT (European patent), AU, BE (European patent), CH (European patent), DE (European patent), FR (European patent), GB (European patent), IT (European patent), JP, KR, LU (European patent), NL (European patent), SE (European patent). Published <i>With international search report.</i> <i>With amended claims.</i>

(54) Title: SYSTEM AND PROCESS FOR VCR SCHEDULING**(57) Abstract**

A VCR schedule controller receives broadcast data over antenna (1) or cable (2) by a programmable tuner (3), which is connected to a teletext receiver (4). The teletext receiver (4) is connected to a microprocessor (5). Microprocessor output (11) is connected to a video display generator (10), used to create text for television receiver (60) to display a message from the microprocessor (5). After processing embedded data in a broadcast, the microprocessor (5) generates a cue for display on TV receiver (60). Remote control receiver (20) receives a command from a remote controller (22) from a viewer input in response to the cue. Remote control receiver (20) supplies a control signal to cause the microprocessor to store the embedded data in memory (9). The microprocessor then issues a message to the display generator (10) as an acknowledgement of the viewer input. The microprocessor (5) monitors the system clock (6) and compares it with stored schedules from the embedded supplemental data. When the system time corresponds to one of the scheduled times, the microprocessor (5) sets the programmable tuner (3) to the stored channel and initiates recording on VCR (30).